

Notice of References Cited	Application/Control No.	Applicant(s)/Patent Under Reexamination	
	10/064,157	GILDRED, JOHN	
	Examiner	Art Unit	Page 1 of 1
Shelton Austin	2112		

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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*	B	US-6,331,852	12-2001	Gould et al.	345/419
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	L	US-			
	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Smith, Douglas J., VHDL & Verilog Compared & Contrasted, Tutorial 48.1, 1996
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.